

Pin assignments for 1024² clock output

Signal name	Data bit	Bit mask (hex)	Connector pin @ DB25
PhiSyncSlow	DataOut 0	1	P5A - 1
PhiSlow1	DataOut 1	2	P5A - 14
PhiSlow2	DataOut 2	4	P5A - 2
PhiEvenOdd	DataOut 3	8	P5A - 15
PhiSyncFast	DataOut 4	10	P5A - 4
PhiFast1	DataOut 5	20	P5A - 17
PhiFast2	DataOut 6	40	P5A - 5
fifo.write	DataOut 7	80	P5A - 18
PhiDeselect1	DataOut 9	200	P5A - 20
PhiDeselect2	DataOut 10	400	P5A - 8
PhiDeselect3	DataOut 11	800	P5A - 21
PhiDeselect4	DataOut 12	1000	P5A - 10
convert	DataOut 16	10000	P5B - 1
VrowOn	DataOut 17	20000	P5B - 14
select	DataOut 18	40000	P5B - 2
VrstRow	DataOut 19	80000	P5B - 15
VrstGlobal	DataOut 20	100000	P5B - 4

The clock out ports are the two *female* DB25 connectors at the top of the DAQ17 board. P5A is the left DB25 as you look into the front panel, and P5B is on the right. There are grounds on pins 16, 19, 22, & 35 and +5V on 3, 6, 9, & 12.