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# NIRSPEC

UCLA Astrophysics Program

U.C. Berkeley

W.M. Keck Observatory

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## NIRSPEC Electronics Design Note 0.00

### Introduction

This document, which will be updated as the design of NIRSPEC progresses, lists the other design notes and their topics, version numbers and dates. Whenever one of us writes or revises one of the design documents, this one should be updated. See George for the location of the file.

The filename should reflect the version number as well as the document number. For instance GSDN0201 is software design note 2, version 1. **Do not** delete old versions when you create new ones. The first (draft) version of any document is version 00, followed by version 01. This will let us have 99 versions of 99 documents of each design category.

Filename	Subject	Ver	Rev date	Author
NEDN0100	Review of system design	00	1-26-95	GB
NEDN0101	System design	01	3-22-95	GB
NEDN0102	Control and Data Acquisition System	02	4-26-95	GB
NEDN0200	Transputer boards	00	2-10-95	GB
NEDN0201	Transputer boards	01	4-26-95	GB
NEDN0202	Transputer boards	02	05-05-95	GB
NEDN0300	Clock Driver/motor controller details	00	pending	FH
NEDN0400	SBRC 1024 <sup>2</sup> array clocking	00	03-03-95	GB
NEDN0401	SBRC 1024 <sup>2</sup> array clocking (superseded by 10.00)	01	03-15-95	GB
NEDN0500	Temperature monitoring	00		FH
NEDN0600	Analog "device" noise	00	4-12-95	WW
NEDN0700	Motor Noise	00	4-3-95	LB
NEDN0800	Selectable Gain and Bandwidth Circuits	00	4-12-95	WW
NEDN0900	Grounding Scheme	00	pending	WW
NEDN1000	Operation of Aladdin 1024 <sup>2</sup> Detector	00	4-26-95	IM
NEDN1100	Electronics system packaging	00	05-05-95	GB
NEDN1101	Electronics system packaging	01	07-27-95	GB
NEDN1102	Electronics system packaging	02	08-22-95	GB
NEDN1103	Electronics system packaging	03	09-28-95	GB

