NIRSPEC

UCLA Astrophysics Program	U.C. Berkeley	W.M. Keck Observatory

NIRSPEC Electronics Application Note 0.00
Index

This document, which will be updated as the implementation of the NIRSPEC electronics progresses, lists the other application notes and their topics, version numbers and dates. Whenever one of us writes or revises one of the programming documents, this one should be updated. See George for the location of the file.

The filename should reflect the version number as well as the document number. For instance NEAN0201 is electronics application note 2, version 1. **Do not** delete old versions when you create new ones. The first (draft) version of any document is version 00, followed by version 01.

Filename	Subject	Ver	Rev date	Author
NEAN0000	Index	00	1-13-99	GB
NEAN0100	System overview	00		GB
NEAN0200	Grounding scheme	00	12-28-98	MA
NEAN0201	Grounding scheme	01	2-19-99	MA
NEAN0300	Power supplies	00	12-28-98	MA
NEAN0400	DAQ15 transputer boards	00	1-13-99	GB
NEAN0500	DAQ17 transputer boards	00	1-13-99	GB
NEAN0600	Pre-amp/A-D boards	00	8-18-98	MA
NEAN0700	Interface boards	00	8-18-98	MA
NEAN0800	Aladdin (1024^2) bias board	00	8-18-98	MA
NEAN0900	PICNIC (256^2) bias boards	00	8-18-98	MA
NEAN1000	Level shifter boards	00	8-18-98	MA
NEAN1100	Packaging scheme	00		GB
NEAN1200	Analog backplane	00		GB
NEAN1300	Transputer (digital) backplane	00	1-13-99	GB
NEAN1400	Stepper motor drives	00		GB
NEAN1500	Calibration unit lamps power & control	00		GB
NEAN1600	Cables list	00	12-9-98	MA
NEAN1700	Diagrams list	00		GB
NEAN1800	Housekeeping transputer crate	00	2-19-99	GB